EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L7	4	(processor$1 and register$1 and clock$1 and multiplexer$1 and asynchronous).clm.	US-PGPUB	OR	OFF	2006/03/23 17:28

IEEE *Xplore* ℗
RELEASE 2.1

☐ Search Results

BROWSE   SEARCH   IEEE XPLORE GUIDE   SUPPORT

Results for "((asynchronous<and>arbitration)<and>multiplexer) <and> (pyr >= 1951 <and>..."
Your search matched **113** of **1331196** documents.
A maximum of **250** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

☑ e-mail 🖨 printer friendly

» Search Options

View Session History

New Search

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

`((asynchronous<and>arbitration)<and>multiplexer) <and> (pyr >= 1951 <and> pyr <=` [**Search** >]

☐ Check to search only within this results set

Display Format: ◉ Citation ○ Citation & Abstract

[**view selected items**]   **Select All Deselect All**   View: **1-25** | 26-50 | 51-75 | 76-100 | 101-113

☐ 1. **Part 3: Carrier sense multiple access with collision detect on (CSMA/CD) access method and physical layer specifications**
IEEE Std 802.3, 2000 Edition
2000 Page(s):i - 1515

AbstractPlus | Full Text: PDF(19532 KB)   IEEE STD

☐ 2. **IEEE recommended practice for futurebus+.**
IEEE Std 896.3-1993
25 July 1994 Page(s):i

AbstractPlus | Full Text: PDF(13404 KB)   IEEE STD

☐ 3. **IEEE standards for local and metropolitan area networks: integrated services (IS) LAN interface at the medium access control (MAC) and physical (PHY) layers**
IEEE Std 802.9-1994
30 Dec. 1994

AbstractPlus | Full Text: PDF(19256 KB)   IEEE STD

☐ 4. **Cell multiplexing in ATM networks**
Rosberg, Z.;
Networking, IEEE/ACM Transactions on
Volume 4, Issue 1, Feb. 1996 Page(s):112 - 122
Digital Object Identifier 10.1109/90.503767

AbstractPlus | Full Text: PDF(1156 KB)   IEEE JNL
Rights and Permissions

☐ 5. **High speed integrated electronics for communications systems**
Berthold, J.E.;
Proceedings of the IEEE
Volume 78, Issue 3, March 1990 Page(s):486 - 511
Digital Object Identifier 10.1109/5.52227

AbstractPlus | Full Text: PDF(3092 KB)   IEEE JNL
Rights and Permissions

☐ 6. **IEEE standards for local and metropolitan area networks: supplement to Integrated Services (IS) LAN interface at the Medium Access Control (MAC) and Physical (PHY) layers: specification of ISLAN16-T**
IEEE Std 802.9a-1995
27 Feb. 1996

AbstractPlus | Full Text: PDF(2100 KB)   IEEE STD

☐ 7. **An autonomous CCSDS packet generating ASIC for analog and digital telemetry**
Reddy, D.; O'Brien, F.; Flynn, M.; McSweeney, C.; Damron, J.; Maguire, G.; Kinsella, B.; Sinander, P.; Smith, C.;

Systems on a Chip (Ref. No. 1998/439), IEE Colloquium on
5 Sept. 1998 Page(s):20/1 - 20/6

AbstractPlus | Full Text: PDF(532 KB)   IEE CNF

8. **A Theoretical Traffic Performance Analysis of an Integrated Voice-Data Virtual Circuit Packet Switch**
    Arthurs, E.; Stuck, B.;
    Communications, IEEE Transactions on [legacy, pre - 1988]
    Volume 27, Issue 7, Jul 1979 Page(s):1104 - 1111

    AbstractPlus | Full Text: PDF(936 KB)   IEEE JNL
    Rights and Permissions

9. **An FPGA for implementing asynchronous circuits**
    Hauck, S.; Burns, S.; Borriello, G.; Ebeling, C.;
    Design & Test of Computers, IEEE
    Volume 11, Issue 3, Autumn/Fall 1994 Page(s):60
    Digital Object Identifier 10.1109/MDT.1994.303848

    AbstractPlus | Full Text: PDF(784 KB)   IEEE JNL
    Rights and Permissions

10. **Digital's personal computers**
    Olsen, K.H.; Folsom, B.J.; Ham, R.J.; Clarke, J.D.;
    Proceedings of the IEEE
    Volume 72, Issue 3, March 1984 Page(s):283 - 299

    AbstractPlus | Full Text: PDF(2319 KB)   IEEE JNL
    Rights and Permissions

11. **Traffic Analysis Tools for Integrated Digital Time-Division Link Level Multiplexing of Synchronous and Asynchronous Message Streams**
    Arthurs, E.; Stuck, B.;
    Selected Areas in Communications, IEEE Journal on
    Volume 1, Issue 6, Dec 1983 Page(s):1112 - 1123

    AbstractPlus | Full Text: PDF(1176 KB)   IEEE JNL
    Rights and Permissions

12. **Metastability behavior of CMOS ASIC flip-flops in theory and test**
    Horstmann, J.U.; Eichel, H.W.; Coates, R.L.;
    Solid-State Circuits, IEEE Journal of
    Volume 24, Issue 1, Feb. 1989 Page(s):146 - 157
    Digital Object Identifier 10.1109/4.16314

    AbstractPlus | Full Text: PDF(860 KB)   IEEE JNL
    Rights and Permissions

13. **High-performance network and channel-based storage**
    Katz, R.H.;
    Proceedings of the IEEE
    Volume 80, Issue 8, Aug. 1992 Page(s):1238 - 1261
    Digital Object Identifier 10.1109/5.158597

    AbstractPlus | Full Text: PDF(2276 KB)   IEEE JNL
    Rights and Permissions

14. **A transparent MAC method for bandwidth sharing and CDV control at the ATM layer of passive optical networks**
    Angelopoulos, J.D.; Venieris, I.S.; Protonotarios, E.N.;
    Lightwave Technology, Journal of
    Volume 14, Issue 12, Dec. 1996 Page(s):2625 - 2635
    Digital Object Identifier 10.1109/50.545781

    AbstractPlus | References | Full Text: PDF(1108 KB)   IEEE JNL
    Rights and Permissions

15. **A new synchronizer design**
    Walker, J.; Cantoni, A.;
    Computers, IEEE Transactions on
    Volume 45, Issue 11, Nov. 1996 Page(s):1308 - 1311
    Digital Object Identifier 10.1109/12.544488

AbstractPlus | References | Full Text: PDF(368 KB)   IEEE JNL
Rights and Permissions

16. **AMULET1: an asynchronous ARM microprocessor**
Woods, J.V.; Day, P.; Furber, S.B.; Garside, J.D.; Paver, N.C.; Temple, S.;
Computers, IEEE Transactions on
Volume 46, Issue 4, April 1997 Page(s):385 - 398
Digital Object Identifier 10.1109/12.588033

AbstractPlus | References | Full Text: PDF(696 KB)   IEEE JNL
Rights and Permissions

17. **An analog VLSI chip with asynchronous interface for auditory feature extraction**
Kumar, N.; Himmelbauer, W.; Cauwenberghs, G.; Andreou, A.G.;
Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [see also
Circuits and Systems II: Express Briefs, IEEE Transactions on]
Volume 45, Issue 5, May 1998 Page(s):600 - 606
Digital Object Identifier 10.1109/82.673642

AbstractPlus | References | Full Text: PDF(148 KB)   IEEE JNL
Rights and Permissions

18. **Design and construction of an optoelectronic crossbar switch containing a terabit per second free-space optical interconnect**
Walker, A.C.; Desmulliez, M.P.Y.; Forbes, M.G.; Fancey, S.J.; Buller, G.S.; Taghizadeh, M.R.;
Dines, J.A.B.; Stanley, C.R.; Pennelli, G.; Boyd, A.R.; Horan, P.; Byrne, D.; Hegarty, J.; Eitel, S.;
Gauggel, H.-P.; Gulden, K.-H.; Gauthier, A.; Benabes, P.; Gutzwiller, J.-L.; Goetz, M.;
Selected Topics in Quantum Electronics, IEEE Journal of
Volume 5, Issue 2, March-April 1999 Page(s):236 - 249
Digital Object Identifier 10.1109/2944.778297

AbstractPlus | References | Full Text: PDF(740 KB)   IEEE JNL
Rights and Permissions

19. **Register-Transfer Level Digital Design Automation: The Allocation Process**
Hafer, L.J.; Parker, A.C.;
Design Automation, 1978. 15th Conference on
19-21 June 1978 Page(s):213 - 219

AbstractPlus | Full Text: PDF(544 KB)   IEEE CNF
Rights and Permissions

20. **Performance evaluation of a general traffic control framework in ATM networks**
Marosits, T.; Molnar, S.; Fodor, G.;
Performance, Computing and Communications Conference, 1999. IPCCC '99. IEEE
International
10-12 Feb. 1999 Page(s):240 - 249
Digital Object Identifier 10.1109/PCCC.1999.749445

AbstractPlus | Full Text: PDF(636 KB)   IEEE CNF
Rights and Permissions

21. **Performance analysis of an ATM switch capable of supporting multiclass traffic**
Seman, K.; Smith, D.G.;
Teletraffic Symposium, 10th. Performance Engineering in Telecommunications Network, Tenth
UK
14-16 Apr 1993 Page(s):20/1 - 2011

AbstractPlus | Full Text: PDF(408 KB)   IEE CNF

22. **IEEE standard for scalable coherent interface (SCI).**
IEEE Std 1596-1992
2 August 1993 Page(s):i

AbstractPlus | Full Text: PDF(5684 KB)   IEEE STD

23. **IEEE standard for VMEbus extensions for instrumentation: VXIbus**
IEEE Std 1155-1992
20 September 1993 Page(s):i

AbstractPlus | Full Text: PDF(1756 KB)   IEEE STD

24. A 4K/spl times/8 dynamic RAM with self-refresh
Reese, E.A.; Spaderna, D.W.; Flannagan, S.T.; Tsang, F.;
Solid-State Circuits, IEEE Journal of
Volume 16, Issue 5, Oct 1981 Page(s):479 - 487

AbstractPlus | Full Text: PDF(1136 KB)   IEEE JNL
Rights and Permissions

25. High-Speed Packet Switching Systems for Multimedia Communications
Ichikawa, H.; Aoki, M.; Uchiyama, T.;
Selected Areas in Communications, IEEE Journal on
Volume 5, Issue 8, Oct 1987 Page(s):1336 - 1345

AbstractPlus | Full Text: PDF(984 KB)   IEEE JNL
Rights and Permissions

IEEE *Xplore* ®
RELEASE 2.1

Welcome United States Patent and Trademark Office

BROWSE   SEARCH   IEEE XPLORE GUIDE   SUPPORT

Results for "((peripheral<and>arbitration)<and>multiplexer) <and> (pyr >= 1951 <and> p..."
Your search matched **52** of **1331196** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

✉ e-mail 🖨 printer friendly

» Search Options

View Session History

New Search

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

((peripheral<and>arbitration)<and>multiplexer) <and> (pyr >= 1951 <and> pyr <= 200   **Search ▶**

☐ Check to search only within this results set

Display Format: ⦿ Citation ○ Citation & Abstract

⎡ **view selected items** ⎤   **Select All Deselect All**   View: **1-25** | 26-50 | 51-52

☐ 1. **High-performance network and channel-based storage**
Katz, R.H.;
Proceedings of the IEEE
Volume 80, Issue 8, Aug. 1992 Page(s):1238 - 1261
Digital Object Identifier 10.1109/5.158597

AbstractPlus | Full Text: PDF(2276 KB)   **IEEE JNL**
Rights and Permissions

☐ 2. **IEEE standard for scalable coherent interface (SCI).**
IEEE Std 1596-1992
2 August 1993 Page(s):i

AbstractPlus | Full Text: PDF(5684 KB)   **IEEE STD**

☐ 3. **A Theoretical Traffic Performance Analysis of an Integrated Voice-Data Virtual Circuit Packet Switch**
Arthurs, E.; Stuck, B.;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 27, Issue 7, Jul 1979 Page(s):1104 - 1111

AbstractPlus | Full Text: PDF(936 KB)   **IEEE JNL**
Rights and Permissions

☐ 4. **Heterogeneous multiprocessor for the management of real-time video and graphics streams**
Strik, M.T.J.; Timmer, A.H.; van Meerbergen, J.L.; van Rootselaar, G.-J.;
Solid-State Circuits, IEEE Journal of
Volume 35, Issue 11, Nov. 2000 Page(s):1722 - 1731
Digital Object Identifier 10.1109/4.881220

AbstractPlus | References | Full Text: PDF(200 KB)   **IEEE JNL**
Rights and Permissions

☐ 5. **MININET: A microprocessor-controlled "Mininetwork"**
Chen, R.C.; Jessel, P.G.; Patterson, R.A.;
Proceedings of the IEEE
Volume 64, Issue 6, June 1976 Page(s):988 - 993

AbstractPlus | Full Text: PDF(632 KB)   **IEEE JNL**
Rights and Permissions

☐ 6. **Digital's personal computers**
Olsen, K.H.; Folsom, B.J.; Ham, R.J.; Clarke, J.D.;
Proceedings of the IEEE
Volume 72, Issue 3, March 1984 Page(s):283 - 299

AbstractPlus | Full Text: PDF(2319 KB)   **IEEE JNL**
Rights and Permissions

7. **GTD-5 EAX hardware overview**
Magnusson, S.; Jinbo, W.;
Communications Magazine, IEEE
Volume 21, Issue 6, Sep 1983 Page(s):6 - 12

AbstractPlus | Full Text: PDF(1072 KB)   IEEE JNL
Rights and Permissions

8. **AMBA: enabling reusable on-chip designs**
Flynn, D.;
Micro, IEEE
Volume 17, Issue 4, July-Aug. 1997 Page(s):20 - 27
Digital Object Identifier 10.1109/40.612211

AbstractPlus | References | Full Text: PDF(424 KB)   IEEE JNL
Rights and Permissions

9. **Architecture and design of NX-2700: a programmable single-chip HDTV all-format-decode-and-display processor**
Dutta, S.;
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on
Volume 9, Issue 2, April 2001 Page(s):313 - 328
Digital Object Identifier 10.1109/92.924054

AbstractPlus | References | Full Text: PDF(232 KB)   IEEE JNL
Rights and Permissions

10. **Microprocessor technology trends**
Myers, G.J.; Yu, A.Y.C.; House, D.L.;
Proceedings of the IEEE
Volume 74, Issue 12, Dec. 1986 Page(s):1605 - 1622

AbstractPlus | Full Text: PDF(2270 KB)   IEEE JNL
Rights and Permissions

11. **Customer Access System Design**
Arnon, E.; Munter, E.; Patel, S.; Roddick, P.; Willcock, P.;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 30, Issue 9, Sep 1982 Page(s):2143 - 2149

AbstractPlus | Full Text: PDF(936 KB)   IEEE JNL
Rights and Permissions

12. **A Broadband Packet Switch for Integrated Transport**
Hui, J.; Arthurs, E.;
Selected Areas in Communications, IEEE Journal on
Volume 5, Issue 8, Oct 1987 Page(s):1264 - 1273

AbstractPlus | Full Text: PDF(968 KB)   IEEE JNL
Rights and Permissions

13. **A Synchronous Fiber Optic Ring Local Area Network for Multigigabit/s Mixed-Traffic Communication**
Bergman, L.; Sverre Eng;
Selected Areas in Communications, IEEE Journal on
Volume 3, Issue 6, Nov 1985 Page(s):842 - 848

AbstractPlus | Full Text: PDF(912 KB)   IEEE JNL
Rights and Permissions

14. **Microcontroller-based architecture for control of a six joint robot arm**
Kabuka, M.R.; Glaskowsky, P.N.; Miranda, J.;
Industrial Electronics, IEEE Transactions on
Volume 35, Issue 2, May 1988 Page(s):217 - 221
Digital Object Identifier 10.1109/41.192652

AbstractPlus | Full Text: PDF(492 KB)   IEEE JNL
Rights and Permissions

15. **The DSP32C: AT&Ts second generation floating point digital signal processor**
Fuccio, M.L.; Gadenz, R.N.; Garen, C.J.; Huser, J.M.; Ng, B.; Pekarich, S.P.; Ulery, K.D.;
Micro, IEEE

AbstractPlus | Full Text: PDF(1864 KB)   IEEE JNL
Rights and Permissions

16. **Video RAMs: structure and applications**
Nicoud, J.-D.;

AbstractPlus | Full Text: PDF(1168 KB)   IEEE JNL
Rights and Permissions

17. **Fault tolerance in commercial computers**
Siewiorek, D.P.;

AbstractPlus | Full Text: PDF(828 KB)   IEEE JNL
Rights and Permissions

18. **The Monarch parallel processor hardware design**
Rettberg, R.D.; Crowther, W.R.; Carvey, P.P.; Tomlinson, R.S.;

AbstractPlus | Full Text: PDF(1612 KB)   IEEE JNL
Rights and Permissions

19. **High speed integrated electronics for communications systems**
Berthold, J.E.;

AbstractPlus | Full Text: PDF(3092 KB)   IEEE JNL
Rights and Permissions

20. **Antifuse field programmable gate arrays**
Greene, J.; Hamdy, E.; Beal, S.;

AbstractPlus | Full Text: PDF(1276 KB)   IEEE JNL
Rights and Permissions

21. **Multi-level decision feedback equalization for saturation recording**
Kenney, J.G.; Carley, L.R.; Wood, R.W.;

AbstractPlus | Full Text: PDF(1020 KB)   IEEE JNL
Rights and Permissions

22. **A real-time MPEG encoder using a programmable processor**
Kim, D.; Young, J.; Milton, S.; Kim, H.J.; Kim, Y.;

AbstractPlus | Full Text: PDF(648 KB)   IEEE JNL
Rights and Permissions

23. **A median peak detecting analog signal processor for hard disk drive servo**
Nagaraj, K.; Lewis, S.H.; Walden, R.W.; Offord, G.E.; Shariatdoust, R.S.; Sabnis, J.A.; Peruzzi, R.O.; Barner, J.R.; Plany, J.; Mento, R.P.; Rakshani, V.A.; Hull, R.W.;

Volume 30, Issue 4, April 1995 Page(s):461 - 470
Digital Object Identifier 10.1109/4.375967

AbstractPlus | Full Text: PDF(960 KB)   IEEE JNL
Rights and Permissions

24. **Simulating Multimedia Systems with MVPSIM**
Jihong Kim; Yongmin Kim;
Design & Test of Computers, IEEE
Volume 12, Issue 4, Wiinter 1995 Page(s):18
Digital Object Identifier 10.1109/MDT.1995.473309

AbstractPlus | Full Text: PDF(816 KB)   IEEE JNL
Rights and Permissions

25. **Telecommunications applications of ferroelectric liquid-crystal smart pixels**
Mears, R.J.; Crossland, W.A.; Dames, M.P.; Collington, J.R.; Parker, M.C.; Warr, S.T.;
Wilkinson, T.D.; Davey, A.B.;
Selected Topics in Quantum Electronics, IEEE Journal of
Volume 2, Issue 1, April 1996 Page(s):35 - 46
Digital Object Identifier 10.1109/2944.541873

AbstractPlus | References | Full Text: PDF(1520 KB)   IEEE JNL
Rights and Permissions

Scholar

Results **1 - 10** of about **188** for **asynchronous multiplexer arbitration**. (0.09 seconds)

Four State **Asynchronous** Architectures - group of 6 »
AJ McAuley - IEEE Transactions on Computers, 1992 - doi.ieeecomputersociety.org
... one cell. The design of an **asynchronous** 2 : 1 **multiplexer** concludes this
section. Sec- 0018-9340/92$03.00 0 1992 IEEE Page 2. 130 ...
Cited by 23 - Web Search

Asynchronous Low Power VLSI Implementation of the International Data Encryption Algorithm - group of 2 »
N Sklavos, O Koufopavlou - Electronics, Circuits and Systems, 2001. ICECS 2001. The 8th ..., 2001 - vlsi.ee.upatras.gr
... **Multiplexer** 4 Keys ... In order to achieve this, **asynchronous** circuits must be
hazard-free ... general signal concurrency must be controlled by **arbitration** in order ...
Cited by 1 - View as HTML - Web Search

[BOOK] Principles of **asynchronous** circuit design: a systems perspective - group of 3 »
J Sparsø, SB Furber - 2001 - imm.dtu.dk
... 77 5.8.2 **Arbitration** 79 5.8.3 Probability of metastability 79 ... 195 11.2.1 An n-way
multiplexer 195 ... 213 Part III Large-Scale **Asynchronous** Designs 13 Descale 221 ...
Cited by 82 - View as HTML - Web Search - Library Search

Self-Calibrating Clocks for Globally **Asynchronous** Locally Synchronous Systems - group of 9 »
SW Moore, GS Taylor, PA Cunningham, RD Mullins, P ... - 2000 International Conference on Computer Design, 2000 -
doi.ieeecomputersociety.org
... For example, whilst changing the delay selection **multiplexer** when a transition is ...
Data **Asynchronous** ... clk is to be rarely delayed by the **arbitration** process then ...
Cited by 20 - Web Search

Asynchronous implementation of the SCPP-A counterflow pipeline processor - group of 4 »
W Korver, IM Nedelchev - IEE Proceedings: Computers and Digital Techniques, 1996 - ieeexplore.ieee.org
... 3 Implementation of the Cop component This Section presents an **asynchronous**
implementation of cop(t) in terms of basic elements ... 4 clearly involves **arbitration**. ...
Cited by 5 - Web Search - BL Direct

Asynchronous Embryonics with Reconfiguration - group of 4 »
AH Jackson, AM Tyrrell - LECTURE NOTES IN COMPUTER SCIENCE, 2001 - elec.york.ac.uk
... logic design, not covered here, where **asynchronous arbitration** takes place ... where
the functional units are **multiplexer** based [2, 3]. **Asynchronous** circuits can ...
Cited by 4 - View as HTML - Web Search - BL Direct

An analog VLSI chip with **asynchronous** interface for auditory feature extraction - group of 6 »
N Kumar, W Himmelbauer, G Cauwenberghs, AG Andreou - IEEE Transactions on Circuits and Systems II Analog and ..., 1998 -
ieeexplore.ieee.org
... KUMAR et al.: ANALOG VLSI CHIP WITH **ASYNCHRONOUS** INTERFACE ... address is applied to
the **multiplexer**, which steers ... The **arbitration** logic and the encoder generate a ...
Cited by 16 - Web Search - BL Direct

... Tools for Integrated Digital Time-Division Link Level Multiplexing of Synchronous and **Asynchronous** ... - group of 3 »
E Arthurs, BW Stuck - IEEE Journal on Selected Areas in Communications, 1983 - ieeexplore.ieee.org
... 1 Fig. 1. Hardware block diagram of link **multiplexer**. ... **asynchronous** stream has the
transmission characteristics of a ... A priority **arbitration** rule is employed to ...
Cited by 4 - Web Search

An FPGA for implementing **asynchronous** circuits - group of 10 »
S Hauck, S Burns, G Borriello, C Ebeling - IEEE Design and Test of Computers, 1994 - doi.ieeecs.org

... designs differ in the **arbitration** mechanisms and ... often be accomplished by a **multiplexer**, which must be ... not specifically targeted to **asynchronous** circuits will ...
<u>Cited by 35</u> - <u>Web Search</u> - <u>BL Direct</u>

<u>Performance Characterization For A Dual-Channel Multimedia **Multiplexer**</u> - <u>group of 2 »</u>
TP Lin - TENCON 90. 1990 IEEE Region 10 Conference on Computer and ..., 1990 - ieeexplore.ieee.org
... Thcrcrorc, pcrfcct channcl **arbitration** can be achieved ... uscd for thc dual-channel
multiplexer are described ... dclay-throughput pcrformance of **asynchronous** data are ...
<u>Web Search</u>

Goooooooooog l e ▶

Result Page: **1** <u>2</u> <u>3</u> <u>4</u> <u>5</u> <u>6</u> <u>7</u> <u>8</u> <u>9</u> <u>10</u>   **Next**

| asynchronous multiplexer arbitration | Search |

<u>Google Home</u> - <u>About Google</u> - <u>About Google Scholar</u>

©2006 Google